

with the DDC/CI protocol for controlling said at least one display;

-- arbitration means having a first input connected to receive said first I²C communication channel provided by said graphics engine, and having a second input receiving a second I²C communication channel provided by said hardware monitoring circuit;

end

-- said arbitration means for providing between said first and said second I²C communication channels so that both hardware monitoring circuit and said processor can get access to said second receiving means of said at least one display without contention.

IN THE ABSTRACT

Page 20	Line 8	Please delete "(21)".
	Line 9	Please delete "(11)".
	Line 10	Please delete "(22)" and "(12)".
	Line 11	Please delete "(11)" and "(22)".
	Line 12	Please delete "(20)".
	Last line	Please delete "Fig. 1.".

REMARKS

This Amendment amends Claims 9 and 13 so that they are no longer multiply dependent. Claims 17 to 20 have been added to provide similar coverage. The Applicants may elect to amend Claims 9 and 13 to make them again multiply dependent or to add additional claims to this application to provide coverage similar to, broader than or narrower than the present claims at any time during the pendency of the above-identified U.S. patent application.

Respectfully submitted,

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